10/662674 10/662674 09/15/03

Transmitted herewith for filing is the Patent Application of:

Inventor: YONG MENG LEE, DA JIN, DAVID VIGAR

For: METHOD OF FORMING DOUBLE-GATED SILICON-ON-INSULATOR (SOI) TRANSISTORS WITH CORNER

ROUNDING

Sir:

Enclosed	i are:						
X	6 sheets of drawing(s) - formal.						
x	An assignment of the invention to Chartered Semiconductor Manufacturing Ltd.						
	An associate power of attorney Applicant claims small entity status						
	Request & Certification under 35 USC 122(b)(2)(b)(i)						
The filing fee has been calculated as shown below:							
	·	(Col. 1)	(Col. 2)	OTHER THAN A	A SMALL ENTITY		
FOR:		NO. FILED	NO. EXTRA	RATE	FEE		
BASIC	FEE	>>	$\geq \leq$		\$ 750.		
TOTAL CLAIMS		48 -20=	28	x 18 =	\$ 504.		
INDE	CLAIMS	3 -3=	0	x 84 =	\$ 0.		
				SUB TOTAL	\$ 1,254.		
				ASSIGNMENT	\$40.		
				TOTAL	\$ 1,294.		
x	Please charge my Deposit Account No. 19-0033 in the amount of \$ 1,294. A duplicate copy of this sheet is enclosed.						
X	The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any						
overpayment to Deposit Account No. 19-0033. A duplicate copy of this sheet is enclosed.							
X Any additional filing fees required under 37 CFR §1.16.							
	XA	X Any patent application processing fees under 37 CFR §1.17.					
Respectfully submitted,							
	SCEPHENB. ACKERMAN, REG. NO. 37,761						

EXPRESS MAIL CERTIFICATE

Express Mail No.EV313927065US

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail in an envelope addressed to Assistant Commissioner of Patents, Alexandria, VA, 22313-1450. Applicant and Attorney requests the date of deposit as the Filing Date.

O 15 03 Date of deposit Signature / Date